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Media Alert

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Siemens collaborates with SK keyfoundry to launch 130nm automotive power semiconductor Calibre PERC PDK

- **Optimizing analog and mixed-signal semiconductor design for improved wafer productivity and yield**
- **Strengthening process variability management through Siemens EDA collaboration, accelerating next-generation process optimization**

Siemens Digital Industries Software announced today that SK keyfoundry, in collaboration with Korea Siemens EDA, has launched a 130nm automotive PDK (Process Design Kit) that is exclusively designed for use in Calibre® PERC™ software. This marks a significant milestone as one of the first Korean domestic established node processes to provide not only schematic verification but also layout verification for interconnect reliability.

With this new PDK, a wide range of Korean domestic and international fabless companies can leverage SK keyfoundry's 130nm process to enhance the design optimization of automotive power semiconductors, while at the same time doing more precise reliability verification.

The 130nm process has long been a stable choice in the analog and power semiconductor markets. However, due to increasing complexity of the designs now using 130nm processes, achieving high-performance semiconductor designs has been challenging. The newly offered SK keyfoundry's PDK allows designers to use Siemens' Calibre PERC solution with SK keyfoundry's process technologies, unleashing innovation and enabling design-stage verification that accounts for manufacturing constraints.

“We are thrilled to introduce the Calibre PERC PDK optimized for 130nm automotive power semiconductor processes in collaboration with Siemens EDA. This PDK is designed to improve design efficiency and reliability, providing our customers with a competitive edge in developing high-performance automotive semiconductor products,” said Ilsup Jin, R&D Senior Vice President, SK keyfoundry. “Moving forward, we plan to continue to strengthen our partnership with Siemens to develop next-generation solutions on established node processes.”

“We are pleased to support this Calibre PERC PDK for 130nm processes, enabling highly reliable design verification in collaboration with SK keyfoundry. This solution is expected to help differentiate SK keyfoundry in the power semiconductor and IoT markets,” said Junan Kim, General Manager, Siemens EDA Korea, Siemens Digital Industries Software. “Siemens remains committed to expanding its collaboration with foundries so that they can leverage the latest EDA technologies regardless of the process nodes they are offering.”

Looking forward, SK keyfoundry plans to further expand its collaboration with Siemens byproviding optimized design solutions, enhanced semiconductor manufacturing reliability, and by strengthening its competitive position in the foundry market.

To learn more about Siemens’ Calibre PERC technology, visit: https://eda.sw.siemens.com/en-US/ic/calibre-design/reliability-verification/perc/

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