

## **Antares' Steven B. Strauss Delivers Speech Titled, 'From the Frying Pan Into the Fire; Working On Both Sides of the Tooling Supply Chain' to International Audience**

VANCOUVER, Wash. and GILBERT, Ariz., April 12, 2007 – Antares Advanced Test Technologies, a global supplier of integrated semiconductor-test consumables focused on maximizing production yields, announced that its vice president of engineering, Steven B. Strauss, was one of four “industry leaders” invited to speak at the eighth annual BiTS Workshop held at the Hilton Phoenix East /Mesa Hotel in Mesa, Ariz., March 11 through March 14, 2007.

The BiTS conference brings together semiconductor test professionals from more than 100 companies around the world to discuss the latest trends in the burn-in and test socket market.

Members of Antares' engineering team joined Strauss at BiTS and presented four research papers to an audience of their peers.

“We certainly think our presence at BiTS affirms that our engineering team is among the most knowledgeable and forward-thinking in the test industry,” said Matt Bergeron, CEO of Antares Advanced Test Technologies. “And, of course, we never miss a chance to talk test with some of our industry's best minds.”

Strauss gave his speech on the night of March 11, during the opening dinner, from 8:00 to 9:00 p.m. MT. Antares' engineers presented the following technical papers on March 13 and March 14:

“Elastomeric Interconnects -- Reliable Enough for Production Test?”; Frank Bumb, Jack Pereschuk of Phoenix Test Arrays with Nick Langston, Sr. of Antares Advanced Test Technologies; March 13; 9:30 a.m. MT

”Socket Signal Integrity -- Impact From IC & Board”; James Zhou, Jiachun (Frank) Zhou of Antares Advanced Test Technologies; March 13; 10:30 a.m. MT

”Minimizing Socket & Board Inductance Using a Novel De-Coupling Interposer”; Nicholas Langston, Sr., James Zhou, Hongjun Yao of Antares Advanced Test Technologies; March 13; 11:00 a.m. MT

”The Importance of the Mechanical Interface in Final Test Efficiency”; Mark Stenholm of Antares Advanced Test Technologies; March 14; 10:30 a.m. MT

For more information on the BiTS Workshop, visit <http://www.bitsworkshop.org/> .

### **About Antares Advanced Test Technologies**

Antares Advanced Test Technologies is reducing the cost of semiconductor test, with

concentration on high-sensitivity areas like yield and integration of test disciplines, by focusing on customer support, delivering innovative technologies and offering a single point of contact for semiconductor test cell requirements including burn-in, ATE consumables and thermal management solutions. Antares is headquartered in Vancouver, Washington and has design, development and manufacturing locations in Suzhou, China; Yokohama, Japan; Phoenix and Gilbert, Arizona; Milpitas and Santa Clara, California; and Aman, Jordan. For more information on Antares, visit <http://www.antares-att.com/>.

**Contacts:**

Antares Advanced Test Technologies

Mark Murdza

Director of Marketing

480-682-6270

[mark.murdza@antares-att.com](mailto:mark.murdza@antares-att.com)

<http://www.antares-att.com/>

Ehrlich Written Communications

Antares Media Relations Contact:

Chris Ehrlich

Principal

503-825-4473

[chris@ewcomm.com](mailto:chris@ewcomm.com)

<http://www.ewcomm.com/>