

For immediate release:



***“The 2011 market value of ‘fan-in’ WLCSP is close to US\$ 1.7B”
announces Yole Développement***

“WLCSP Market & Industrial Trends”, a report from Yole Développement

Lyon, France – Dec. 20, 2011 – Yole Développement announces its report “WLCSP Market & Industrial Trends”. High growth rate, different technologies – thus different players – are the main trends identified by Yole Développement

Growth rate is high, but the market shows early signs of maturity

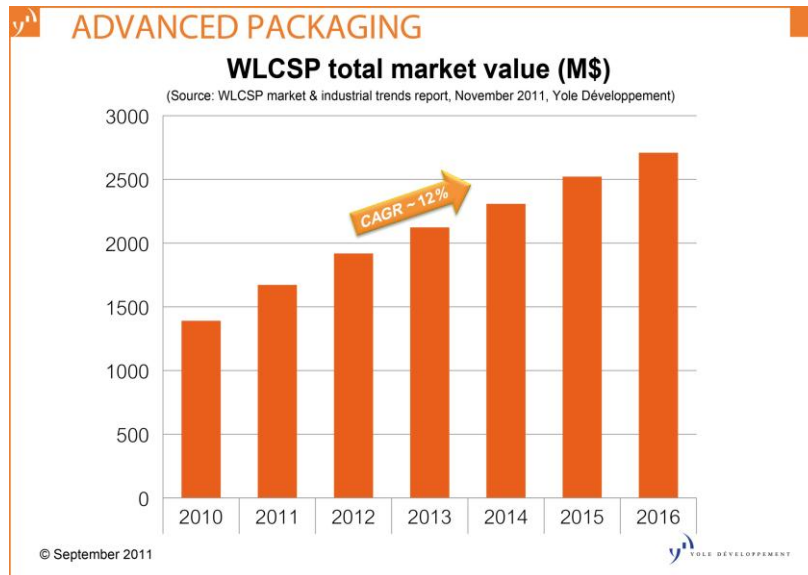
Many “WLP” technologies are now cruising at a very high altitude. This is serious business: over 23 billion units packaged with ‘fan-in’ as we may call it are expected to sell and be mounted in smartphones, tablet PCs or other mobile devices in 2011. Size reduction and cost, remain the main drivers for adoption of this technology. If ‘fan-in’ WLCSP already reached high penetration rates in mobile applications for connectivity (Bluetooth + WLAN + FM combos, GPS) and analogue integrated circuits (DC/DC converters, LDO’s, ESD/EMI protection devices), it is still growing fast for some other IC types as well as in MEMS and sensors.

Reaching 2.3 million 300mm equivalent ‘fan-in’ WLCSP wafers in 2011 for a total market size of \$1.7B

The average size of a ‘fan-in’ WLCSP device tended to increase over the past 2 years, with many “connectivity” devices reaching sizes of more than 30mm². *“In 2011, more than 2.3 million 300mm equivalent ‘fan-in’ WLCSP wafers will be processed for the first time,”* estimates Jean-Marc Yannou, Senior Analyst, Advanced Packaging at Yole Développement. *“All steps compounded (wafer level, die level and test), we estimate that the 2011 market value of ‘fan-in’ WLCSP is close to \$ 1.7B.”*

Prices keep on decreasing, but the market growth rate is expected to stay high at 12% over the 2010-2016 time period

Although costs and prices are decreasing as the technology and its market mature, the market value growth rate for the 2010-2016 timeframe is still expected around 12%, 4 points higher than semiconductor packaging in general. However, this is lower than the 22% 2008-2013 growth rate Yole Développement had computed for the same market 2 years ago. Costs kept on decreasing as the capacity shortage on 12-inch diameter wafers ended and process flows were standardized and simplified. While technologies have improved to the point that the maximum possible die size could be extended, it is not all clear skies for ‘fan-in’ WLCSP as the fallout of Nokia, WLCSP’s historical biggest supporter, on the handsets market continues, and some tier two handset manufacturers remain reluctant to adopt the technology.



Changing paradigms : future growth will be driven by different devices, based on different technologies

Looming over the horizon is the threat of the CMOS 28nm node technology with such a high IO density that some package with a “fan-out area” will be needed anyhow. Hopefully, a significant part of the ‘fan-in’ WLCSP market still relies on the analog ICs on the one hand, using older technologies, and on MEMS and sensors (particularly CMOS image sensors) on the other hand. This latter device type is expected to be a high potential growth application for fan-in WLCSP in the coming 5 years: Yole Développement expects a tremendous growth of up to 25% for MEMS and sensors with WLCSP over the next 5 years. All in all, ‘fan-in’ WLCSP shows the first early signs of a maturing market with price pressure process standardization, but it still grows faster than the average semiconductor packaging market due to the fast growth rates of smartphones and tablet PCs in which WLCSP considerably helps save space and reduce costs.

High margins are attracting new player types with diverse business

The supply chain is still primarily led by OSATs and IDMs, but the original “Wafer bumping houses” and “wafer packaging houses” take a significant share of the market too. All the major OSATs invested in 300mm WLCSP capacity over the past 2 years. TSMC recently announced their intention to step in this market too, possibly opening the way to other foundries.

In this WLCSP 2011 report, you will find detailed technical and market status and forecasts on WLCSP technologies and applications. Market forecasts and growth rates are provided based on device units and wafers for each market segment over the 2010-2016 timeframe. Market value forecasts in dollars are given over the same period of time. Based on Yole Développement’s “bottom-up” analysis of the WLP fabs, the report displays the list and ranking of the WLCSP front-end (RDL, UBM and balling) players as of end of 2010, including the detailed respective wafer production capacities by player and wafer type.

This “bottom-up meets top-down analysis” cross checks Yole Développement’s forecasting models application by application, and player by player, delivering a high definition picture of the status of the ‘fan-in’ WLCSP market, with, for example, the level of loading of the major WLCSP fabs by

application and by design company.

Numerous application examples are given, recent technical developments on materials, architectures and test are detailed, and industry-wide technology roadmaps are presented. In addition, the report provides for the analysis of the supply chain and a detailed cost analysis section with models and examples.

About WLCSP Market & Industrial Trends report:

- **Authors:**

Jean-Marc Yannou joined Yole Développement as technology and market expert in the fields of advanced packaging and Integrate Passive Devices. He has 15-years of experience in the semiconductor industry. He worked for Texas Instruments and Philips (then NXP semiconductor) where he served as Innovation Manager for System-in-Package technologies.

Lionel Cadix joined Yole Développement after the completion of several projects linked to the characterization and modeling of high density TSV and 3DIC chip stacking in collaboration with CEA Leti and STMicroelectronics during his PhD. He is author of several publications and 8 patents in the field of 3D Integration.

- **Catalogue price:** Euros 3,990.00 (single user license) - Publication date: Nov. 2011. For special offers and the price in dollars, please contact David Jourdan (jourdan@yole.fr or +33 472 83 01 90).

- **Companies cited in the report:**

Accretech, Advantest, Agilent Technologies, Alphaprobes, Amkor, AMS, Anadigics, Analog Devices, AnalogicTech, Apple, Applied Materials, Asahi Kasei (AKM), ASE, Audience, Avago Technologies, Bosch, Broadcom, Cascade Microtech, Casio Micronics, China WLCSP, Cirrus Logic, CSR, Cypress, Delphi, Dialog Semiconductors, Discera, DNP, Dow, Dupont, electroglas, Ellipsiz, EPWorks, Fairchild, FCI, Form factor, Fraunhofer IZM, Freescale, Fujikura, Fujitsu, Google, Hamamatsu, Hynix, ICI, Idex, IDT, IME, imec, Infineon, International Rectifier, Intersil, Invensense, JCAP, JEM, JSR Micro, LAM Research, LB Semicon, Lenovo, LETI, Linear Technology, LTX/Credence, Marvell, Maxim, Maxlinear, Microchip, Micron, Micronics, Mitsubishi Chemical, Mosart, Motorola, Murata, National Semiconductors, Nemotek, Nepes, Nexx, Nippon Steel, Materials, Nokia, NXP, OKI, Okmetic, Omnivision, On semiconductor, Optopac, PacTech, Panasonic, PowerTech (PTI), Qualcomm, Renesas, Rhom, RIM (Blackberry), Samsung, Sanyo, Sensoror, Sharp, Shibasoku, Shin-Etsu, Shinko, Silecs, Silex, Siltech, SiTime, SMSC, Sony, SPIL, SPTS, StatsChipPac, STEricsson, STMicroelectronics, Sumitomo, Süss Microtec, TDK-Epcos, technoprobe, TEL, Teledyne/Dalsa, Teradyne, TeraMikros, TeraProbe, Texas Instruments, Toray, Toshiba Semiconductors, Tronics Microsystems, TSMC, UMC, Unisem, Verigy, Visera, VTI, Wentworth laboratories, Wolfson Microelectronics, Xintec, Yamaha, Yokogawa ...

About Yole Developpement – www.yole.fr

Beginning in 1998 with Yole Développement, we have grown to become a group of companies providing market research, technology analysis, strategy consulting, media in addition to finance services. With a solid focus on emerging applications using silicon and/or micro manufacturing Yole Développement group has expanded to include more than 40 associates worldwide covering MEMS, Microfluidics & Medical, Advanced Packaging, Compound Semiconductors, Power Electronics, LED, and Photovoltaic. The group supports companies, investors and R&D organizations worldwide to help them understand markets and follow technology trends to develop their business.

CUSTOM STUDIES

- Market data, market research and marketing analysis
- Technology analysis
- Reverse engineering and reverse costing
- Strategy consulting
- Corporate Finance Advisory (M&A and fund raising)

MEDIA

- Critical news, Bi-weekly: Micronews, the magazine
- In-depth analysis & Quarterly Technology Magazines: MEMS Trends– 3D Packaging – PV Manufacturing – iLED – Power Dev¹
- Online disruptive technologies website: www.i-micronews.com
- Exclusive Webcasts
- Live event with Market Briefings

TECHNOLOGY & MARKET REPORTS

- Collection of reports
 - Players & market databases
 - Manufacturing cost simulation tools
 - Component reverse engineering & costing analysis
- More information on www.yole.fr

CONTACTS

For more information about :

- Services : Jean-Christophe Eloy (eloy@yole.fr)
- Reports: David Jourdan (jourdan@yole.fr)
- Media : Sandrine Leroy (leroy@yole.fr)

###